

REFRESH RATE ADJUSTMENT

Abstract

5 The present invention is a random access memory device assembled into
a chip package with a refresh request control circuit, and a method for using the
same. The refresh request control circuit includes a first circuit configured to
receive a first refresh rate signal with a first refresh rate signal frequency. The
first circuit produces a second refresh rate signal with a second refresh rate
signal frequency. The refresh request control circuit also includes a second
10 circuit configured to receive the first refresh rate signal and the second refresh
rate signal. The refresh request control circuit also includes a final fusing
element coupled to the second circuit. The final fusing element selects a final
refresh rate signal. The selection of the final refresh rate signal occurs after the
random access memory device is assembled into a chip package.